

# Semiconductor IC and IP Development (DE0208)

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## Semiconductor IC and IP Development according to ISO 26262

### Who should attend

- Project Managers
- Safety Managers
- Concept Engineers
- Design and Verification Engineers (Hardware, System)
- Application and Field Application Engineers
- Quality managers
- Project Team and Automotive Business Group Leaders

**Duration:** 4 days (or in-house, jointly agreed, please contact us for more information).

At the end of the 4th day there is a possibility to do the [FSP](#) exam. This test is optional and free of charge.

**Language:** Depending on the participants, the training will be given in German or English. The training material will be in English.

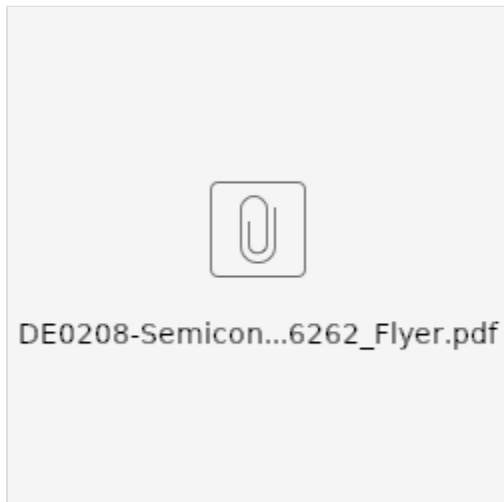
**Location:** exida.com GmbH office

Prof.-Messerschmitt-Straße 1  
D-85579 Neubiberg / Germany

or **ONLINE**

**Certificate:** Each participant gets a letter of attendance.

### Brochure



### Course topics:

- General Functional Safety Management
  - Overview of Functional Safety and ISO 26262
  - Functional Safety Management
  - Automotive Safety Lifecycle
  - Safety Lifecycle Tailoring for IC and IP Development Projects
  - Safety Plan and Safety Case
  - Confirmation Measures and Verification Reviews
  - Functional Safety Assessment and Certification
  - Supporting Processes: Configuration and Change Management
- Item Definition and Concept Phase
  - Item Definition
  - Hazard Analysis & Risk Assessment
  - Functional Safety Concept and Functional Safety Requirements
  - Safety Element out of Context (SEooC) Definition
  - Supporting Processes: Specification and Management of Safety Requirement
- System Development
  - Technical Safety Concept and Technical Safety Requirements
  - ASIL Decomposition
  - Dependent Failure Analysis (DFA)
  - Hardware-Software Interface Specification
  - System Integration and Testing, Validation
- IC and IP Hardware Development
  - Hardware Safety Requirements
  - Hardware Safety Architecture and Design Specification
  - Safety Functions and Safety Mechanisms for Semiconductors
  - Hardware Design Implementation and Verification, and Special
  - Requirements for Semiconductor Development Projects
  - Safety Manual
  - Supporting processes: Confidence in the Use of Software Tools
- Safety Analyses
  - Overview of Safety Analyses
  - Qualitative and Quantitative Fault Tree Analysis
  - Failure Modes, Effects and Diagnostics Analysis (FMEDA)
  - Failure Rates Estimation
  - Failure Modes and Failure Mode Distribution
  - Estimation and Verification of Diagnostic Coverage
  - Relative and Absolute Metrics (SPFM, LFM, PMHF)

Scheduled courses - [Register here:](#)